

**Listing of Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Original) A communication system comprising:  
a first device transmitting a modulated signal; and  
a second device receiving the modulated signal, the second device including  
a first demodulator receiving the modulated signal, producing a first demodulated output and implementing a first demodulation technique,  
a second demodulator receiving the modulated signal, producing a second demodulated output and implementing a second demodulation technique, the second demodulation technique differing from the first demodulation technique, and  
an error detection module performing bit error detection based on the first demodulated output and the second demodulated output.
2. (Original) The communication system as set forth in claim 1, and wherein the first demodulation technique is differential-frequency shift keying and the second demodulation technique is offset quadrature phase shift keying.
3. (Original) The communication system as set forth in claim 1, and wherein the modulated signal is modulated using a packetized protocol.
4. (Original) The communication system as set forth in claim 3, and wherein the error detection module performs bit error detection by comparing the first demodulator output with the second demodulator output on a packet-by-packet basis.
5. (Original) The communication system as set forth in claim 1, and wherein the first device implements a frequency shift keying modulation technique.

6. (Original) The communication system as set forth in claim 5, and wherein the first device uses a non-minimum shift keying nominal modulation index.
7. (Original) A method of transferring data via a radio frequency signal, the method comprising: converting data into a digital signal; modulating the digital signal using a packetized protocol to generate a modulated signal; wirelessly transferring the modulated signal; receiving the modulated signal; demodulating the modulated signal to produce a demodulated signal; and implementing an error detection algorithm.
8. (Original) The method of claim 7, and further comprising demodulating the modulated signal using a first demodulation technique and a second demodulation technique, the first demodulation technique producing a first demodulated signal and the second demodulation technique producing a second demodulated signal.
9. (Original) The method of claim 8, and further comprising implementing an error detection algorithm based on the first demodulated signal and the second demodulated signal.
10. (Original) The method of claim 9, and further comprising comparing the first demodulated signal with the second demodulated signal on a packet-by-packet basis.
11. (Original) The method of claim 9, and wherein implementing an error detection algorithm includes outputting a second digital signal based on the first demodulated signal and the second demodulated, the second digital signal being substantially identical to the first digital signal.

Claims 12-33 (Cancelled)